Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (currently amended) A flat thermionic emission screen (1) comprising:

a first substrate (2) on which are arranged an emission cathode (4) and an electron extraction grid (8),

a second substrate (12) facing the first substrate (2), on which is arranged an anode (14) designed to collect the electrons emitted by the cathode(4), and

an electronic control circuit (19) of the anode voltage (14) comprising at least one commutation component (18,20), wherein the commutation component (18,20) is ~~integrated through design in the first substrate (2) and in the second substrate (12) of the screen (1)~~ an HV transistor with a first electrode (40, 50) integrated in the first substrate (2), a second electrode (42,52) integrated in the second substrate (12) and a third electrode (44,54) integrated in the extraction grid (8).

2. (canceled)

3. (previously presented) The screen according to claim 1, wherein the anode (14) constitutes the emissive surface of the screen (1) and comprises at least one conducting 25 surface (15) on which phosphor materials (17) are deposited.

4. (currently amended) The screen according to claim ~~2~~ 1, wherein the cathode(4) comprises conducting columns (32) with sources of electrons (6), and in that the grid (8) 30 comprises perforated conducting lines crossing the said conducting lines at the positions of the said sources (6).

5. (previously presented) The screen according to claim 4, wherein the cathode (4) is a microtip source.

6. (previously presented) The screen according to claim 4, wherein the cathode (2) is a nanotube source.

7. (currently amended) The screen according to claim 1, wherein the electronic circuit (19) is of the push-pull type ~~and comprises furthermore a second transistor (20) with a first electrode (50) integrated in the first substrate (2), a second electrode (52) integrated in the second substrate (12) and connected electrically to the first electrode (50) of the first transistor (18), and at third control electrode (54) integrated in the extraction grid (8) of the screen (1)~~.

8. (previously presented) The screen according to claim 1, further comprising a logical control module (100) comprising a low voltage source Vdd (24) and an optoelectronic coupler (26).

9. (previously presented) The Screen according to claim 8, wherein the logical control module (100) comprises a load pump galvanically insulated from the screen (1) by a capacitor C.

10-13. (canceled)